

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

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05/25/04

Title of Invention

METHOD OF FORMING A SEMICONDUCTOR DEVICE
HAVING AIR GAPS AND THE STRUCTURE SO FORMED

Application Number : 10/709722

Confirmation Number:

First Named Applicant: Anthony Stamper

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Art Unit:

Examiner:

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(6603204 or 6596624 or 6596467 or 6589644 or 6577011 or 6479374 or 6413852
or 6387824 or 6362091 or 6319854 or 6287979 or 6265780 or 5994776 or 5965202
or 5548159 or 5470801 or 5468685 or 5461003 or 20030114013).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
CC	1	6603204	2003-08-05	Gates et al		257	760
CC	2	6596624	2003-07-22	Romankiw		438	619
CC	3	6596467	2003-07-22	Gallagher et al		430	314
CC	4	6589644	2003-07-08	Yamada et al		428	312.6
CC	5	6577011	2003-06-10	Buchwalter et al		257	758
CC	6	6479374	2002-11-12	Ioka et al		438	601
CC	7	6413852	2002-07-02	Grill et al		438	619
CC	8	6387824	2002-05-14	Aoi		438	778
CC	9	6362091	2002-03-26	Andideh et al		438	624
CC	10	6319854	2001-11-20	Aoi		438	781
CC	11	6287979	2001-09-11	Zhou et al		438	723
CC	12	6265780	2001-07-24	Yew et al		257	759
CC	13	5994776	1999-11-30	Fang et al		257	758
CC	14	5965202	1999-10-12	Taylor-Smith et al		427	245
CC	15	5548159	1996-08-20	Jeng		257	634
CC	16	5470801	1995-11-28	Kapoor et al		438	471
CC	17	5468685	1995-11-21	Orisaka et al		438	694
CC	18	5461003	1995-10-24	Havemann et al		438	666

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
C.C.	1	20030114013	2003-06-19	Hedrick et al		438	734

Signature

Examiner Name	Date
	6/16/05